1. A method of computing a drift reduction block for use in reducing drift in a block of quantized MPEG discrete cosine transform coefficients, comprising:

processing a block of discrete cosine transform coefficients by dropping at least one coefficient in the block;

forming a dropped coefficient block containing the at least one coefficient;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

2. The method according to claim 1, wherein the processing comprises dropping at least one coefficient in the block containing high frequency coefficients.

3. The method according to claim 1, wherein the processing comprises dropping a plurality of high frequency coefficients.

4. A method of computing a drift reduction frame for use in reducing drift in a frame comprising blocks of quantized MPEG discrete cosine transform coefficients, comprising:

for each block in the frame:

processing a block of discrete cosine transform coefficients by dropping at least one coefficient in the block;

forming a dropped coefficient block containing the at least one coefficient;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

5. The method according to claim 4, wherein the processing comprises dropping at least one coefficient in the block containing high frequency coefficients.

6. The method according to claim 4, wherein the processing comprises dropping a plurality of high frequency coefficients.

7. The method according to claim 4, further comprising mapping a block of video coefficients to a corresponding block of coefficients in the drift reduction frame using a motion vector.

8. The method according to claim 7, further comprising discrete cosine transforming the block of coefficients in the drift reduction frame.

9. The method according to claim 8, further comprising quantizing the discrete cosine transformed block of coefficients in the drift reduction frame.

10. A method of reducing drift in a block of quantized MPEG discrete cosine transform coefficients, comprising:

computing a drift reference block of discrete cosine transform coefficients;

mapping the drift reference block to a block of quantized video coefficients, the block of quantized discrete cosine transformed video coefficients having at least one dropped coefficient using a motion vector; and

adding the coefficients of the drift reference block to the coefficients of the block of quantized video coefficients that have not been blocked to form a drift compensated block.

11. The method according to claim 10, further comprising variable length coding the drift compensated block.

12. The method according to claim 10, further comprising repeating the computing, mapping and adding for each motion vector in a frame of MPEG video.

13. The method according to claim 10, wherein the computing is carried out by:

forming a dropped coefficient block containing at least one coefficient dropped in quantizing the block of quantized MPEG discrete cosine transform coefficients;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

14. The method according to claim 13, further comprising discrete cosine transforming the block of coefficients in the drift reduction block.

15. The method according to claim 14, further comprising quantizing the discrete cosine transformed block of coefficients in the drift reduction frame.

16. An MPEG transcoder, comprising:

a variable length decoder (VLD) receiving an MPEG bitstream and produces variable length decoded video frames and motion vectors that characterize movement of objects in the video frames;

a processor that processes the frames of VLD decoded video by dropping high frequency discrete cosine transform coefficients in blocks of data;

a drift reference frame creator that creates a drift reference frame by, for each block in the video frame:

processing a block of discrete cosine transform coefficients by deleting at least one coefficient in the block;

forming a dropped coefficient block containing the at least one coefficient;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block;

a drift compensator that compensates for drift in video blocks in the frame by, for each motion vector pointing to the frame:

using the motion vector, mapping a block in the drift reference frame to a block of quantized discrete cosine transformed video coefficients having at least one dropped coefficient;

discrete cosine transforming the block of coefficients in the drift reduction block; and

adding the coefficients of the drift reference block to the coefficients of the block of quantized video coefficients that have not been dropped to form a drift compensated block; and

variable length coding the drift compensated block.

17. A method of drift compensating a current frame of MPEG video, the current frame having a motion vector associated therewith, comprising:

dropping pixels from a reference frame of video;

decoding the dropped pixels to form a drift reference frame;

mapping a block of video from the current frame to a block in the drift reference frame; and

compensating the block of video from the current frame using the block in the drift reference frame.

18. The method according to claim 17, wherein the current frame of MPEG video has a plurality of motion vectors, and wherein the mapping and compensating are carried out for each of the motion vectors.

19. The method according to claim 17, wherein the decoding comprises:

forming a dropped coefficient block containing the at least one coefficient;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

20. The method according to claim 17, wherein the dropping comprises dropping at least one coefficient in the block containing high frequency coefficients.

21. The method according to claim 17, wherein the dropping comprises dropping a plurality of high frequency coefficients.

22. The method according to claim 17, wherein the compensating comprises adding the block of video from the current frame to the block in the drift reference frame.

23. An MPEG transcoder having drift compensation that compensates a current frame of MPEG video, the current frame having a motion vector associated therewith, comprising:

means for dropping pixels from a reference frame of video;

a decoder for decoding the dropped pixels to form a drift reference frame;

mapping means for mapping a block of video from the current frame to a block in the drift reference frame; and

a drift compensator that compensates the block of video from the current frame using the block in the drift reference frame.

24. The apparatus according to claim 23, wherein the current frame of MPEG video has a plurality of motion vectors, and wherein the mapping means and drift compensator map and compensate for each of the motion vectors.

25. The apparatus according to claim 23, wherein the decoder decodes the dropped pixels by:

forming a dropped coefficient block containing the at least one coefficient;

inverse quantizing the at least one coefficient to produce an inverse quantized dropped coefficient block; and

inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

26. The apparatus according to claim 23, wherein the dropping comprises dropping at least one coefficient in the block containing high frequency coefficients.

27. The apparatus according to claim 23, wherein the means for dropping drops a plurality of high frequency coefficients.

28. The method according to claim 23, wherein the compensator compensates by adding the block of video from the current frame to the block in the drift reference frame.

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29. An MPEG transcoder having drift compensation, comprising:

means for forming a dropped coefficient block containing the at least one coefficient;

an inverse quantizer that inverse quantizes the at least one coefficient to produce an inverse quantized dropped coefficient block; and

an inverse discrete cosine transformer for inverse discrete cosine transforming the inverse quantized dropped coefficient block to produce the drift reduction block.

30. The apparatus according to claim 29, wherein the forming means comprises means for dropping at least one coefficient in the block containing high frequency coefficients.

31. The apparatus according to claim 29, wherein the forming means drops a plurality of high frequency coefficients.

32. An electronic storage medium storing instructions which, when executed on a programmed processor, carry out a method of reducing drift in a block of quantized MPEG discrete cosine transform coefficients, comprising:

computing a drift reference block of discrete cosine transform coefficients;

mapping the drift reference block to a block of quantized video coefficients, the block of quantized discrete cosine transformed video coefficients having at least one dropped coefficient using a motion vector; and

adding the coefficients of the drift reference block to the coefficients of the block of quantized video coefficients that have not been blocked to form a drift compensated block.